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022878 7590 02/18/2004

AGILENT TECHNOLOGIES, INC.
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DEPT.
P.O. BOX 7599
M/S DL429
LOVELAND, CO 80537-0599

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<u>Shirley A. Gonzales</u>	(Depositor's name)
<u>Shirley A. Gonzales</u>	(Signature)
<u>02/25/04</u>	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/634,068

08/08/2000

David N. Sontag

10001979-1

3576

TITLE OF INVENTION: SIGNAL TRANSITION AND STABLE REGIONS DIAGRAM FOR POSITIONING A LOGIC ANALYZER SAMPLE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional

NO

\$1330

\$0

\$1330

05/18/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
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SUAREZ, FELIX E

2857

702-068000

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1 Regan L. Trumper

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Agilent Technologies, Inc.

Palo Alto, CA

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature)

(Date)

2/25/04

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